

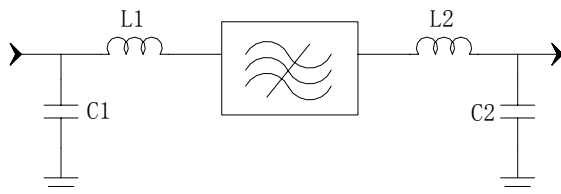
### Specifications

Parameter	Unit	Minimum	Typical	Maximum
Center Frequency	MHz	95.9	96	96.1
Insertion Loss	dB	-	13.9	16
1 dB Bandwidth	MHz	5	5.52	-
3 dB Bandwidth	MHz	-	6.14	-
15 dB Bandwidth	MHz	-	7.21	8
40 dB Bandwidth	MHz	-	8.91	-
50 dB Bandwidth	MHz	-	10.07	-
Passband Variation (in 1dB Bandwidth)	dB	-	0.3	0.8
Absolute Delay	usec	-	1.45	
Phase Linearity( $f_0 \pm 2.5\text{MHz}$ )		-	4	
Group Delay Variation (in 1dB Bandwidth)	nsec	-	30	80
Ultimate Rejection(40 to 87MHz)	dB	43	56	-
Ultimate Rejection(111 to 150MHz)	dB	43	61	-
Material Temperature coefficient	KHz/°C		-1.728	
Ambient Temperature	°C		25	
Package Size		SMD13.3*6.5		

#### Notes:

1. All specifications are based on the test circuit shown
2. In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
4. This is the optimum impedance in order to achieve the performance show

### Matching Configuration



$$L1=L2=180\text{nH}$$

$$C1=C2=47\text{pF}$$

Source/Load Impedance=50 ohm

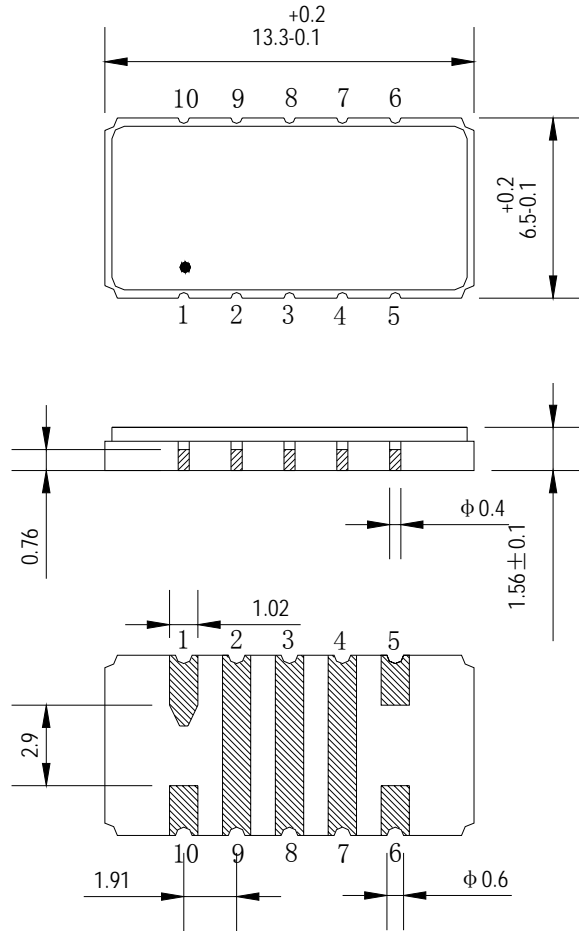
Notes - Component values may change depending on board layout.



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*Package Dimension*



**Input:1**  
**Output:6**

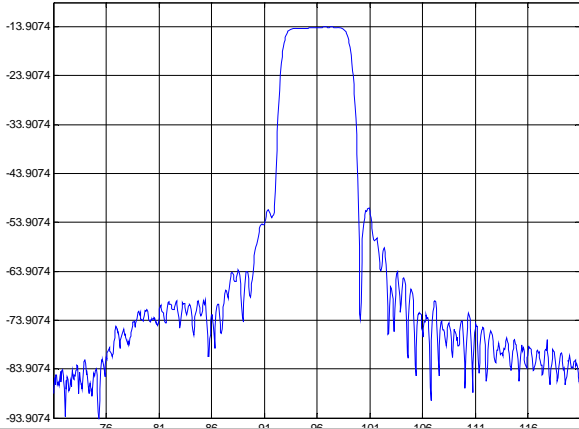


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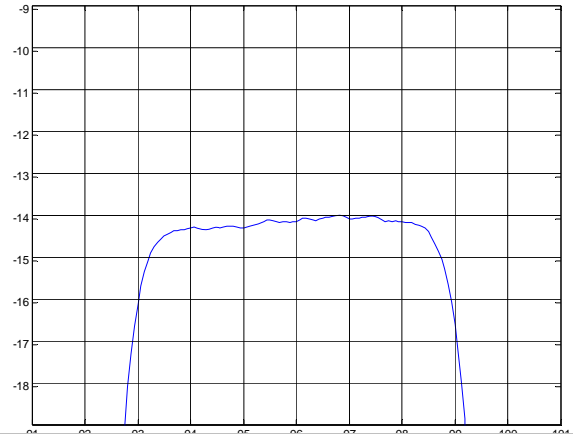
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Typical Performance

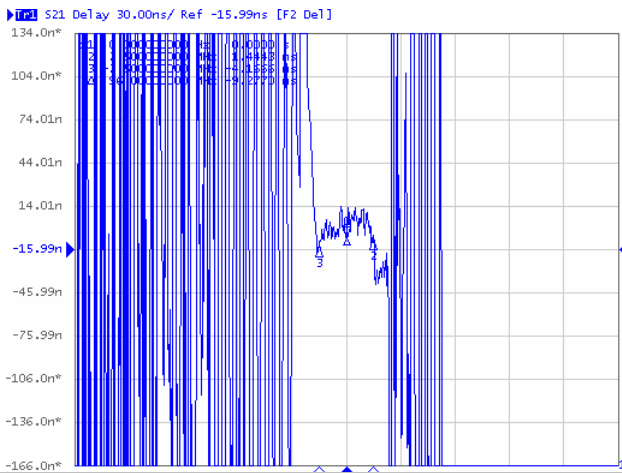
Frequency Respond



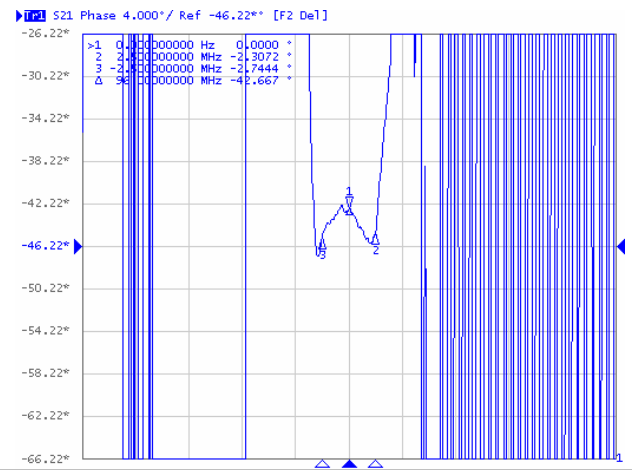
Passband Respond



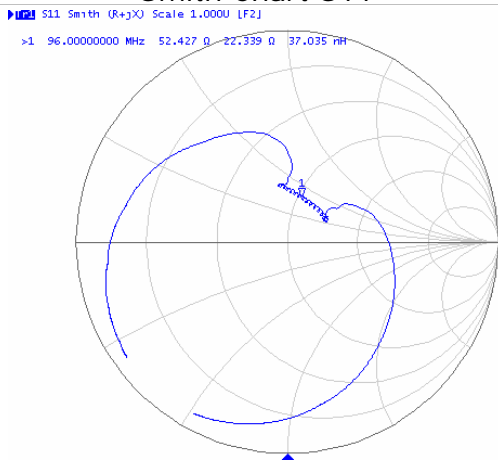
Group Delay Variation( $f_0 \pm 2.5$ MHz)



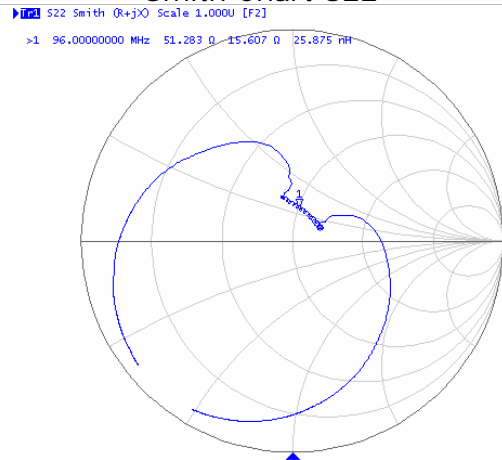
Phase Linearity( $f_0 \pm 2.5$ MHz)



Smith Chart S11



Smith Chart S22



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